

### REMARKS

Claims 1-18, 27-78, 80 and 81 are pending with claims 1-12 and 81 being independent. Claims 1-10, 13-18, 27-78 and 80 have been withdrawn from consideration, leaving independent claims 11, 12 and 81 under consideration. Claims 19-26 have been canceled, claims 11 and 12 have been amended, and claim 81 has been added. Support for the amendments and the new claim may be found in the application at, for example, Fig. 9 and the associated text at page 21, line 10 to page 22, line 26. No new matter has been introduced.

Claims 11 and 12 have been rejected as being anticipated by Koyama (U.S. Patent Publication No. 2001-0048408). Applicant requests reconsideration and withdrawal of this rejection because Koyama does not describe or suggest a current source configured to receive a set signal and a signal current, and to control an output current value corresponding to the signal current, as recited in each of amended independent claims 11 and 12. For example, the storage circuit of Fig. 5B of Koyama, which the rejection equates with the recited current source, comprises an SRAM current source that does not control an output current corresponding to an input signal current and, instead, sets an output potential based on an input potential. Accordingly, for at least this reason, the rejection should be withdrawn.

Claim 81 similarly recites a plurality of current source circuits, each of which is supplied a first current and supplies a second current corresponding to the first current. Accordingly, claim 81 is allowable over Koyama for the reasons discussed above with respect to claims 11 and 12.

Applicant submits that all claims are in condition for allowance.

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Respectfully submitted,

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